

Generalized Digital Redesign Method for Linear Feedback System Based on N-Delay Control

Hiroshi Fujimoto, *Student Member, IEEE*, Atsuo Kawamura, *Senior Member, IEEE*,
and Masayoshi Tomizuka, *Fellow, IEEE*

Abstract— A new digital redesign method is proposed for feedback control systems, by which the states in the continuous-time system are completely reserved in the redesigned sampled-data system. The features of the proposed method are: 1) the N-delay control is employed, and the l th plant input is changed N_l times during one sampling period; 2) the states of the redesigned sampled-data system completely match those of the original continuous-time closed-loop system at every sampling period; and 3) the proposed redesign method can be applicable for a static state-feedback and/or a dynamic controller. An illustrative example of position control using a dc servo motor is presented. The advantages of the proposed redesign approach are demonstrated both in the time and frequency domains.

Index Terms— Digital control, digital redesign, multirate sampling control, N-delay control.

I. INTRODUCTION

OWING TO THE recent developments in computer and interface hardware, digital controllers are usually utilized for controlling robots or motors because of cost, reliability, flexibility, compactness, and so on.

The digital redesign is a technique by which an analog controller designed in continuous time is converted to an equivalent digital controller [1]. It is, in general, very difficult to let the states of the digital system match those of the original continuous-time system.

Historically, one of the most popular digital redesign methods is the Tustin (or bilinear) transformation, in which an s -domain analog controller is transformed to a z -domain digital controller by

$$s = \frac{2(z-1)}{T(z+1)}. \quad (1)$$

This approach is straightforward, and the stable poles of the controller in the s -domain are mapped inside of the unit circle in the z -domain. However, the closed-loop stability is not assured. Therefore, in this approach, the feedback system may become unstable if the sampling time is set too large.

In [1]–[3], the digital redesign methods based on the closed-loop characteristics were developed. However, these attempts

did not assure the closed-loop stability, because approximations were assumed for obtaining solutions of the digital redesign (see Section II). In [4], the feedforward and feedback gains are altered at every sampling period, so that the states of the two systems match at the end of N sampling periods. The method in [4] is similar to the proposed method only in some special cases. However, the number of times of gain alternation, N , is redundant compared to the proposed method in this paper.

The method in [1] was further investigated in [5], and a different approximation was proposed for obtaining a solution of the digital redesign, in which the closed-loop stability was maintained. However, the transition matrices of the original and approximately redesigned systems are not the same. Furthermore, the methods in [1]–[5] have a limitation, in that the original continuous-time controller must be of static state-feedback type and all of the plant state variables must be directly detected.

The other digital redesign methods for a dynamic controller were developed in [6]–[8]. These methods tried to match closed-loop frequency response approximately. However, because of using these approximations, the closed-loop stability could not be assured [6], [7], or the solution of the digital redesign could not be obtained in a large sampling period [8].

Multirate digital controls have been proposed for applications on the pole/zero assignment problem, strong stabilization, simultaneous stabilization, adaptive control, and so on [9]. However, this paper makes the first attempt to apply the multirate digital control to the digital redesign problem.

The purpose of the proposed method is to develop a new digital controller from the analog controller so that all of the states of the sampled-data closed-loop system completely match those of the original continuous-time closed-loop system at every sampling instance. Thus, the stability of the closed-loop system is retained, and the transition matrices of the two systems become identical. In the proposed method, the multirate-input digital control is employed, and the l th plant input is changed N_l times during one sampling period [10]. References [11] and [12] called this method N-delay control, after [13]. The digital redesigned controller can be automatically calculated following the proposed procedure. Moreover, the redesign method for an observer is also presented, in which the multirate-output digital control is employed. Therefore, the proposed method can deal with the system even if a part of the plant states is not directly detected. The proposed digital redesign method is applicable both to a continuous-time static

Manuscript received November 21, 1997; revised January 5, 1999. Recommended by Technical Editor K. Ohnishi.

H. Fujimoto is with the Department of Electrical Engineering, University of Tokyo, Tokyo 113-8656, Japan.

A. Kawamura is with the Department of Electrical and Computer Engineering, Yokohama National University, Yokohama 240-8501, Japan.

M. Tomizuka is with the Department of Mechanical Engineering, University of California, Berkeley, CA 94720-1740 USA.

Publisher Item Identifier S 1083-4435(99)04809-7.

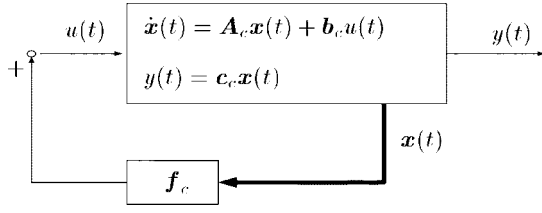


Fig. 1. Continuous-time state-feedback control system.

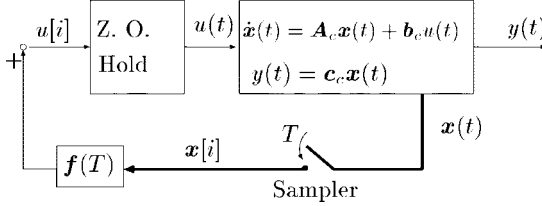


Fig. 2. Discrete-time state-feedback control system.

state-feedback controller and a dynamic controller.

II. PROBLEM OF DIGITAL REDESIGN BY COMPLETE STATE MATCHING

In this section, we consider the problem of matching the responses of an existing continuous-time system shown in Fig. 1, with those of the discrete-time system shown in Fig. 2 for the same initial conditions. Consider the linear continuous-time system described by

$$\dot{\mathbf{x}}(t) = \mathbf{A}_c \mathbf{x}(t) + \mathbf{b}_c u(t). \quad (2)$$

Also, let the continuous-time state-feedback control law be

$$u(t) = \mathbf{f}_c \mathbf{x}(t). \quad (3)$$

The continuous-time closed-loop system becomes

$$\dot{\mathbf{x}}(t) = (\mathbf{A}_c + \mathbf{b}_c \mathbf{f}_c) \mathbf{x}(t) \quad (4)$$

and its sampled-data system with the sampling period T is

$$\mathbf{x}((i+1)T) = e^{(\mathbf{A}_c + \mathbf{b}_c \mathbf{f}_c)T} \mathbf{x}(iT). \quad (5)$$

Consider the discrete-time system utilizing a zero-order hold described by

$$\mathbf{x}[i+1] = \mathbf{A} \mathbf{x}[i] + \mathbf{b} u[i] \quad (6)$$

where $\mathbf{x}[i] = \mathbf{x}(iT)$, $\mathbf{A} \triangleq e^{\mathbf{A}_c T}$, and $\mathbf{b} \triangleq \int_0^T e^{\mathbf{A}_c \tau} d\tau \mathbf{b}_c$. Letting the discrete-time state-feedback control law be $u[i] = \mathbf{f}(T) \mathbf{x}[i]$, the discrete-time closed-loop system becomes

$$\mathbf{x}[i+1] = (\mathbf{A} + \mathbf{b} \mathbf{f}(T)) \mathbf{x}[i]. \quad (7)$$

From (5) and (7), the digital redesign problem is to find the discrete-time gain $\mathbf{f}(T)$ from the continuous-time gain \mathbf{f}_c so that the equation

$$\mathbf{A} + \mathbf{b} \mathbf{f}(T) = e^{(\mathbf{A}_c + \mathbf{b}_c \mathbf{f}_c)T} \quad (8)$$

is satisfied. If the above condition is satisfied, the states of the digitally controlled system in (7) completely match those of the continuous-time system in (5) at every sampling period T . The

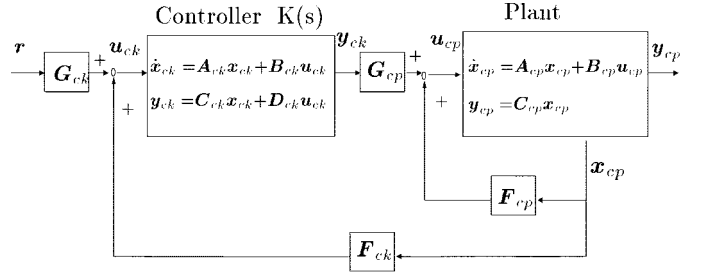


Fig. 3. Continuous-time control system.

existence of $\mathbf{f}(T)$ in (8), however, is not always guaranteed, because the dimension of the input is generally less than that of the state. Therefore, in [1] and [2], (8) is approximately solved for $\mathbf{f}(T)$, but, because of the approximation, the stability of the obtained digital closed-loop system is not always assured, and the time response is different from that of the continuous-time feedback system.

III. DIGITAL REDESIGN BY N-DELAY CONTROL

In this section, a new digital redesign method by the N-delay input control is presented, in which the l th plant input is changed N_l times during one sampling period. The introduction of the N-delay input control causes the increase of the input dimension, thus, (8) can be solved without any approximation. Moreover, the proposed method is applicable either to a continuous-time dynamic controller or a static state-feedback controller. The proposed method succeeds in this generalization by the introduction of: 1) the closed-loop augmented system consisting of the plant and the dynamic controller and 2) the N-delay input control.

Consider the continuous-time plant described by

$$\dot{\mathbf{x}}_{cp}(t) = \mathbf{A}_{cp} \mathbf{x}_{cp}(t) + \mathbf{B}_{cp} \mathbf{u}_{cp}(t) \quad (9)$$

$$\mathbf{y}_{cp}(t) = \mathbf{C}_{cp} \mathbf{x}_{cp}(t) \quad (10)$$

where the plant state $\mathbf{x}_{cp} \in \mathbf{R}^{n_p}$, the plant input $\mathbf{u}_{cp} \in \mathbf{R}^{m_p}$, and the plant output $\mathbf{y}_{cp} \in \mathbf{R}^{p_p}$. As shown in Fig. 3, let the continuous-time control law using a state-feedback and/or dynamic controller be

$$\mathbf{u}_{cp}(t) = \mathbf{F}_{cp} \mathbf{x}_{cp}(t) + \mathbf{G}_{cp} \mathbf{y}_{ck}(t) \quad (11)$$

where the dynamic controller's output $\mathbf{y}_{ck} \in \mathbf{R}^{p_k}$. Let the dynamic controller $K(s)$ be represented by

$$\dot{\mathbf{x}}_{ck}(t) = \mathbf{A}_{ck} \mathbf{x}_{ck}(t) + \mathbf{B}_{ck} \mathbf{u}_{ck}(t) \quad (12)$$

$$\mathbf{y}_{ck}(t) = \mathbf{C}_{ck} \mathbf{x}_{ck}(t) + \mathbf{D}_{ck} \mathbf{u}_{ck}(t) \quad (13)$$

where the dynamic controller's state $\mathbf{x}_{ck} \in \mathbf{R}^{n_k}$, input $\mathbf{u}_{ck} \in \mathbf{R}^{m_k}$. Let the controller's input be given by

$$\mathbf{u}_{ck}(t) = \mathbf{F}_{ck} \mathbf{x}_{cp}(t) + \mathbf{G}_{ck} \mathbf{r}(t) \quad (14)$$

where the reference input $\mathbf{r} \in \mathbf{R}^{m_r}$ is piecewise constant, i.e., $\mathbf{r}(t) = \mathbf{r}(iT)$ for $iT \leq t < (i+1)T$. From (9) to (14), the continuous-time closed-loop augmented system consisting of the plant and the dynamic controller is represented by

$$\dot{\bar{\mathbf{x}}}(t) = \bar{\mathbf{A}} \bar{\mathbf{x}}(t) + \bar{\mathbf{B}} \mathbf{r}(t) \quad (15)$$

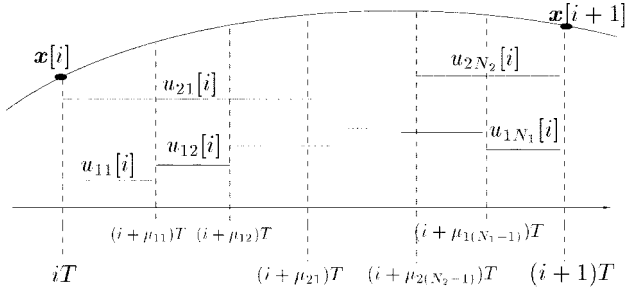


Fig. 4. N-delay input control.

where

$$\begin{aligned} \bar{\mathbf{A}}_c &\triangleq \begin{bmatrix} \mathbf{A}_{cp} + \mathbf{B}_{cp}\mathbf{F}_{cp} + \mathbf{B}_{cp}\mathbf{G}_{cp}\mathbf{D}_{ck}\mathbf{F}_{ck} & \mathbf{B}_{cp}\mathbf{G}_{cp}\mathbf{C}_{ck} \\ \mathbf{B}_{ck}\mathbf{F}_{ck} & \mathbf{A}_{ck} \end{bmatrix} \\ \bar{\mathbf{B}}_c &\triangleq \begin{bmatrix} \mathbf{B}_{cp}\mathbf{G}_{cp}\mathbf{D}_{ck}\mathbf{G}_{ck} \\ \mathbf{B}_{ck}\mathbf{G}_{ck} \end{bmatrix} \\ \bar{\mathbf{x}}_c &\triangleq \begin{bmatrix} \mathbf{x}_{cp} \\ \mathbf{x}_{ck} \end{bmatrix} \end{aligned} \quad (16)$$

and the sampled-data system for the sampling period T becomes

$$\bar{\mathbf{x}}_c((i+1)T) = \bar{\mathbf{A}}\bar{\mathbf{x}}_c(iT) + \bar{\mathbf{B}}\mathbf{r}(iT) \quad (17)$$

where

$$\begin{aligned} \bar{\mathbf{A}} &\triangleq e^{\bar{\mathbf{A}}_c T} \triangleq \begin{matrix} n_p & n_k \\ \begin{bmatrix} \bar{\mathbf{A}}_{11} & \bar{\mathbf{A}}_{12} \\ \bar{\mathbf{A}}_{21} & \bar{\mathbf{A}}_{22} \end{bmatrix} \\ n_k \end{matrix} \\ \bar{\mathbf{B}} &\triangleq \int_0^T e^{\bar{\mathbf{A}}_c \tau} \bar{\mathbf{B}}_c d\tau \triangleq \begin{matrix} m_r \\ n_p & n_k \\ \begin{bmatrix} \bar{\mathbf{B}}_1 \\ \bar{\mathbf{B}}_2 \end{bmatrix} \\ n_k \end{matrix} \end{aligned} \quad (18)$$

$$\bar{\mathbf{B}} \triangleq \int_0^T e^{\bar{\mathbf{A}}_c \tau} \bar{\mathbf{B}}_c d\tau \triangleq \begin{matrix} m_r \\ n_p & n_k \\ \begin{bmatrix} \bar{\mathbf{B}}_1 \\ \bar{\mathbf{B}}_2 \end{bmatrix} \\ n_k \end{matrix} \quad (19)$$

The redesigned digital control system is required for the exact matching of the states $\bar{\mathbf{x}}_c$ at every sampling instant. In the N-delay control, the l th ($l = 1, 2, \dots, m_p$) plant input is changed N_l times during one sampling period, as shown in Fig. 4. (N_1, \dots, N_{m_p}) are referred to as input multiplicities [10]. The selections of the N_l are made by the following condition:

$$N_l \geq \sigma_l \quad (20)$$

where $(\sigma_1, \dots, \sigma_{m_p})$ are a set of generalized controllability indexes of $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ which are introduced by authors of [14], and which are defined as follows.

Definition 1: Generalized controllability indexes of $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ are defined as follows. If $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ is a controllable pair, n_p linearly independent vectors can be selected from

$$\{\mathbf{b}_{c1}, \dots, \mathbf{b}_{cm_p}, \mathbf{A}_{cp}\mathbf{b}_{c1}, \dots, \mathbf{A}_{cp}\mathbf{b}_{cm_p}, \dots, \mathbf{A}_{cp}^{n_p-1}\mathbf{b}_{cm_p}\}$$

where $\mathbf{B}_{cp} = [\mathbf{b}_{c1}, \dots, \mathbf{b}_{cm_p}]$. Letting φ be a set of these n_p vectors, σ_l are defined by

$$\sigma_l = \text{number} \{k | \mathbf{A}_{cp}^{k-1}\mathbf{b}_{cl} \in \varphi\} \quad (21)$$

$$\sum_{l=1}^{m_p} \sigma_l = n_p. \quad (22)$$

In the case of the single-input plant ($m_p = 1$), this index is simply the same number as the plant order ($\sigma_1 = n_p$). The above definition includes that of Kronecker invariants or controllability indexes defined in [10].

The discrete-time plant using N-delay input control is given by

$$\mathbf{x}_{dp}[i+1] = \mathbf{A}_{dp}\mathbf{x}_{dp}[i] + \sum_{l=1}^{m_p} \sum_{j=1}^{N_l} \mathbf{b}_{lj}u_{lj}[i] \quad (23)$$

where $\mathbf{x}_{dp} \in \mathbf{R}^{n_p}$ is the plant state, $u_{lj}[i] \in \mathbf{R}^1$ is the l th plant input for $(i + \mu_{l(j-1)})T \leq t < (i + \mu_{lj})T$ ($l = 1, \dots, m_p, j = 1, \dots, N_l$), and

$$\begin{aligned} \mathbf{A}_{dp} &\triangleq e^{\mathbf{A}_{cp}T} \\ \mathbf{b}_{lj} &\triangleq \int_{(i-\mu_{lj})T}^{(i-\mu_{l(j-1)})T} e^{\mathbf{A}_{cp}\tau} \mathbf{b}_{cl} d\tau, \end{aligned} \quad (24)$$

$$0 = \mu_{l0} < \mu_{l1} < \mu_{l2} < \dots < \mu_{lN_l} = 1. \quad (25)$$

As shown in Fig. 5, let the dynamics of the discrete-time controller be

$$\mathbf{x}_{dk}[i+1] = \mathbf{L}_1\mathbf{x}_{dp}[i] + \mathbf{L}_2\mathbf{x}_{dk}[i] + \mathbf{L}_3\mathbf{r}[i] \quad (26)$$

where the discrete-time dynamic controller's state $\mathbf{x}_{dk} \in \mathbf{R}^{n_k}$. Let the discrete-time control law be

$$\mathbf{u}_{lj}[i] = \mathbf{f}_{lj}\mathbf{x}_{dp}[i] + \mathbf{g}_{lj}\mathbf{x}_{dk}[i] + \mathbf{h}_{lj}\mathbf{r}[i]. \quad (27)$$

From (23) to (27), the discrete-time closed-loop augmented system is represented by

$$\bar{\mathbf{x}}_d[i+1] = \bar{\mathbf{A}}_d\bar{\mathbf{x}}_d[i] + \bar{\mathbf{B}}_d\mathbf{r}[i] \quad (28)$$

where

$$\begin{aligned} \bar{\mathbf{A}}_d &\triangleq \begin{bmatrix} \mathbf{A}_{dp} + \mathbf{BF} & \mathbf{BG} \\ \mathbf{L}_1 & \mathbf{L}_2 \end{bmatrix} \\ \bar{\mathbf{B}}_d &\triangleq \begin{bmatrix} \mathbf{BH} \\ \mathbf{L}_3 \end{bmatrix}, \quad \bar{\mathbf{x}}_d \triangleq \begin{bmatrix} \mathbf{x}_{dp} \\ \mathbf{x}_{dk} \end{bmatrix} \end{aligned} \quad (29)$$

$$\mathbf{B} \triangleq [\mathbf{b}_{11}, \dots, \mathbf{b}_{1N_1}, \mathbf{b}_{21}, \dots, \mathbf{b}_{m_p N_{m_p}}] \quad (n_p \times N)$$

$$\mathbf{F} \triangleq [\mathbf{f}_{11}^T, \dots, \mathbf{f}_{1N_1}^T, \mathbf{f}_{21}^T, \dots, \mathbf{f}_{m_p N_{m_p}}^T]^T \quad (N \times n_p)$$

$$\mathbf{G} \triangleq [\mathbf{g}_{11}^T, \dots, \mathbf{g}_{1N_1}^T, \mathbf{g}_{21}^T, \dots, \mathbf{g}_{m_p N_{m_p}}^T]^T \quad (N \times n_k)$$

$$\mathbf{H} \triangleq [\mathbf{h}_{11}^T, \dots, \mathbf{h}_{1N_1}^T, \mathbf{h}_{21}^T, \dots, \mathbf{h}_{m_p N_{m_p}}^T]^T \quad (N \times m_r)$$

$$N \triangleq N_1 + N_2 + \dots + N_{m_p} \geq n_p.$$

Comparing (17) and (28), if the following conditions are satisfied, the states of the digitally controlled system ($\bar{\mathbf{x}}_c$) completely match the states of the continuous-time system ($\bar{\mathbf{x}}_d$) at every sampling period:

$$\begin{bmatrix} \bar{\mathbf{A}}_{11} & \bar{\mathbf{A}}_{12} \\ \bar{\mathbf{A}}_{21} & \bar{\mathbf{A}}_{22} \end{bmatrix} = \begin{bmatrix} \mathbf{A}_{dp} + \mathbf{BF} & \mathbf{BG} \\ \mathbf{L}_1 & \mathbf{L}_2 \end{bmatrix} \quad (30)$$

$$\begin{bmatrix} \bar{\mathbf{B}}_1 \\ \bar{\mathbf{B}}_2 \end{bmatrix} = \begin{bmatrix} \mathbf{BH} \\ \mathbf{L}_3 \end{bmatrix}. \quad (31)$$

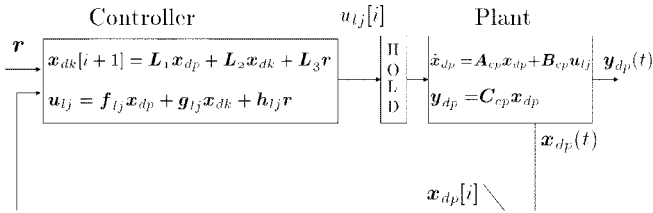


Fig. 5. Discrete-time control system.

Here, the necessary and sufficient condition for solution of the linear matrix equation ($\mathbf{Ax} = \mathbf{b}$) is

$$\text{rank } \mathbf{A} = \text{rank} [\mathbf{A}, \mathbf{b}]. \quad (32)$$

Therefore, the necessary and sufficient conditions for the existence of $\mathbf{F}, \mathbf{G}, \mathbf{H}$ in (30) and (31) are given by

$$\begin{aligned} \text{rank } \mathbf{B} &= \text{rank} [\mathbf{B}, \bar{\mathbf{A}}_{11} - \mathbf{A}_{dp}] \\ &= \text{rank} [\mathbf{B}, \bar{\mathbf{A}}_{12}] = \text{rank} [\mathbf{B}, \bar{\mathbf{B}}_1]. \end{aligned} \quad (33)$$

Concerning the matrix \mathbf{B} , the next theorem is proved in [14], if σ_l is the generalized controllability index. In the case of the controllability index, it is also proved in [10].

Theorem 1: Let $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ be a controllable pair. If the input multiplicities satisfy $N_l \geq \sigma_l$ for $(l = 1, 2, \dots, m_p)$, for almost all μ_j ($l = 1, 2, \dots, m_p, j = 1, \dots, N_l - 1$) and almost all T , the matrix \mathbf{B} has full row rank, i.e.,

$$\text{rank } \mathbf{B} = n_p. \quad (34)$$

Because of this theorem, the row rank \mathbf{B} in (33) is full, thus, (33) is satisfied. As a result, the existence of $\mathbf{F}, \mathbf{G}, \mathbf{H}$ in (30) and (31) is assured. Solving (30) and (31), the redesigned parameters are given by

$$\begin{aligned} \mathbf{F} &= \mathbf{B}^- (\bar{\mathbf{A}}_{11} - \mathbf{A}_{dp}) \\ \mathbf{G} &= \mathbf{B}^- \bar{\mathbf{A}}_{12} \\ \mathbf{H} &= \mathbf{B}^- \bar{\mathbf{B}}_1 \\ L_1 &= \bar{\mathbf{A}}_{21} \\ L_2 &= \bar{\mathbf{A}}_{22} \\ L_3 &= \bar{\mathbf{B}}_2 \end{aligned} \quad (35)$$

where \mathbf{B}^- is the generalized inverse of matrix \mathbf{B} [15].

Comments: 1) if $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ is a controllable pair, the system, which is controlled by either a continuous-time dynamic controller or a static state-feedback controller, can be always redesigned in this method; 2) if the original continuous-time system is stably designed, the redesigned system is assured to be stable because the two transition matrices (30) become identical. Moreover, (30) can guarantee the intersample stability [16]; and 3) the states of the redesigned sampled-data system completely match those of the original continuous-time closed-loop system at every sampling period, independent of sampling period. Therefore, the proposed method is superior to the conventional method, such as Tustin transformation, and other methods [1]–[8].

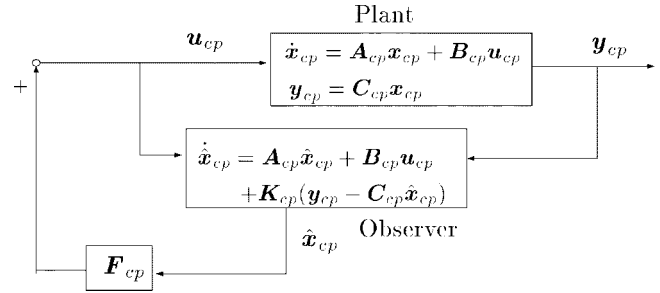


Fig. 6. Continuous-time observer.

IV. DIGITAL REDESIGN FOR OBSERVER

Because the redesigned control laws obtained in Section III are state-feedback control, as shown in (26) and (27), all states need to be detected directly. However, in the general case, all states are not always detected directly, and the calculation time delay may not be negligible. Therefore, in this section, the discrete-time state observer is considered to feed back the estimated plant state $\hat{\mathbf{x}}[i]$ instead of the plant state $\mathbf{x}[i]$.

In this section, the digital redesign method for the state observer which is based on the multirate-output sampling is proposed by using the duality of the redesign method for the controller.

Consider the designed continuous-time observer shown in Fig. 6 for the continuous-time plant (9) described by

$$\dot{\hat{\mathbf{x}}}_{cp}(t) = \mathbf{A}_{cp} \hat{\mathbf{x}}_{cp}(t) + \mathbf{B}_{cp} \mathbf{u}_{cp}(t) + \mathbf{K}_{cp} (\mathbf{y}_{cp} - \mathbf{C}_{cp} \hat{\mathbf{x}}_{cp}(t)) \quad (36)$$

where the estimated plant state is $\hat{\mathbf{x}}_{cp}$. Because the estimation error of the continuous-time state \mathbf{e}_{cp} becomes

$$\begin{aligned} \dot{\mathbf{e}}_{cp}(t) &= \dot{\mathbf{x}}_{cp}(t) - \dot{\hat{\mathbf{x}}}_{cp}(t) \\ &= (\mathbf{A}_{cp} - \mathbf{K}_{cp} \mathbf{C}_{cp}) \mathbf{e}_{cp}(t). \end{aligned} \quad (37)$$

The sampled-data system for the sampling period T is represented by

$$\mathbf{e}_{cp}((i+1)T) = e^{(\mathbf{A}_{cp} - \mathbf{K}_{cp} \mathbf{C}_{cp})T} \mathbf{e}_{cp}(iT). \quad (38)$$

In the proposed method, the multirate-output sampling control is employed, in which the q th plant output is detected M_q times during one sampling period, as shown in Fig. 7, and the discrete-time estimation errors (\mathbf{e}_{dp}) completely match the continuous-time estimation errors (\mathbf{e}_{cp}) at every sampling period. This period is also called the frame period [17]. This control is a duality scheme of the N-delay input control, and it is also called the M-delay output control. The selections of output multiplicities M_q are made by the following condition.:

$$M_q \geq \rho_q \quad (39)$$

where $(\rho_1, \dots, \rho_{p_n})$ are a set of generalized observability indexes of $(\mathbf{A}_{cp}, \mathbf{C}_{cp})$ which are defined as follows.

Definition 2: Generalized observability indexes of $(\mathbf{A}_{cp}, \mathbf{C}_{cp})$ are defined as follows. If $(\mathbf{A}_{cp}, \mathbf{C}_{cp})$ is an observable pair, n_p linearly independent vectors can be selected from

$$\{\mathbf{c}_{c1}, \dots, \mathbf{c}_{cp}, \mathbf{c}_{c1} \mathbf{A}_{cp}, \dots, \mathbf{c}_{cp} \mathbf{A}_{cp}, \dots, \mathbf{c}_{cp} \mathbf{A}_{cp}^{n_p-1}\}$$

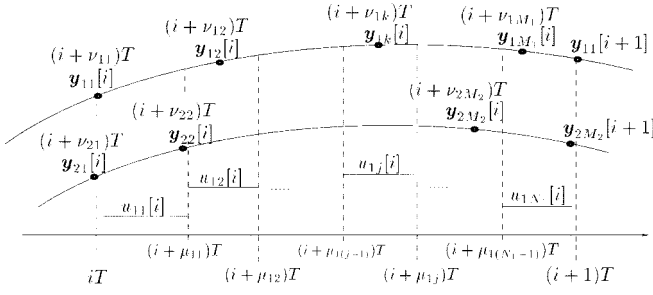


Fig. 7. M-delay output control.

where $\mathbf{C}_{cp} = [\mathbf{c}_{c1}^T, \dots, \mathbf{c}_{cp}^T]^T$. Letting φ be a set of these n_p vectors, ρ_q are defined by

$$\rho_q = \text{number } \{k | \mathbf{c}_{cq} \mathbf{A}_{cp}^{k-1} \in \varphi\} \quad (40)$$

$$\sum_{q=1}^{p_p} \rho_q = n_p. \quad (41)$$

The discrete-time plant using N-delay input control and M-delay output control is given by

$$\mathbf{x}_{dp}[i+1] = \mathbf{A}_{dp} \mathbf{x}_{dp}[i] + \sum_{l=1}^{m_p} \sum_{j=1}^{N_l} \mathbf{b}_{lj} u_{lj}[i] \quad (42)$$

$$y_{qk}[i] = \mathbf{c}_{qk} \mathbf{x}_{dp}[i] + \sum_{l=1}^{m_p} \sum_{j=1}^{N_l} d_{qklj} u_{lj}[i] \quad (43)$$

where

$$\mathbf{c}_{qk} \triangleq \mathbf{c}_{cq} e^{\mathbf{A}_{cp} \nu_{qk} T} \quad (44)$$

$$d_{qklj} \triangleq \begin{cases} \mu_{lj} < \nu_{qk}: & \mathbf{c}_{cq} \int_{(\nu_{qk} - \mu_{lj})T}^{(\nu_{qk} - \mu_{lj})T} e^{\mathbf{A}_{cp} \tau} \mathbf{b}_{cl} d\tau \\ \mu_{lj} < \nu_{qk} \leq \mu_{lj}: & \mathbf{c}_{cq} \int_0^{(\nu_{qk} - \mu_{lj})T} e^{\mathbf{A}_{cp} \tau} \mathbf{b}_{cl} d\tau \\ \nu_{qk} \leq \mu_{lj}: & 0, \\ 0 \leq \nu_{q1} < \nu_{q2} < \dots < \nu_{qM_q} < 1 & \end{cases} \quad (45)$$

and $y_{qk} \in \mathbf{R}^1$ is the q th plant output on $t = (i + \nu_{qk})T$ ($q = 1, 2, \dots, p_p, k = 1, 2, \dots, M_q$).

Using the intersampling outputs y_{qk} , let the discrete-time observer be

$$\begin{aligned} \hat{\mathbf{x}}_{dp}[i+1] &= \mathbf{A}_{dp} \hat{\mathbf{x}}_{dp}[i] + \sum_{l=1}^{m_p} \sum_{j=1}^{N_l} \mathbf{b}_{lj} u_{lj}[i] + \sum_{q=1}^{p_p} \sum_{k=1}^{M_q} \\ &\cdot \mathbf{k}_{qk} \left\{ y_{qk}[i] - \left(\mathbf{c}_{qk} \hat{\mathbf{x}}_{dp}[i] + \sum_{l=1}^{m_p} \sum_{j=1}^{N_l} d_{qklj} u_{lj}[i] \right) \right\} \end{aligned} \quad (46)$$

where the state of the discrete-time observer $\hat{\mathbf{x}}_{dp} \in \mathbf{R}^{n_p}$. The estimation error of the discrete-time state \mathbf{e}_{dp} is represented by

$$\begin{aligned} \mathbf{e}_{dp}[i+1] &= \mathbf{x}_{dp}[i+1] - \hat{\mathbf{x}}_{dp}[i+1] \\ &= (\mathbf{A}_{dp} - \mathbf{K}\mathbf{C}) \mathbf{e}_{dp}[i] \end{aligned} \quad (47)$$

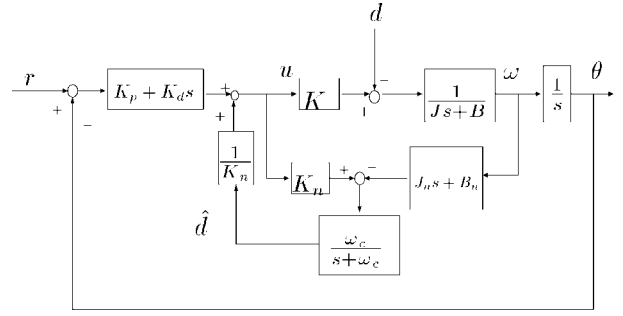


Fig. 8. The position control with the disturbance observer.

where

$$\begin{aligned} \mathbf{K} &\triangleq [\mathbf{k}_{11}, \dots, \mathbf{k}_{1M_1}, \mathbf{k}_{21}, \dots, \mathbf{k}_{p_p M_{p_p}}] \quad (n_p \times M) \\ \mathbf{C} &\triangleq [\mathbf{c}_{11}^T, \dots, \mathbf{c}_{1M_1}^T, \mathbf{c}_{21}^T, \dots, \mathbf{c}_{p_p M_{p_p}}^T]^T \quad (M \times n_p) \\ M &= M_1 + M_2 + \dots + M_{p_p} \geq n_p. \end{aligned} \quad (48)$$

Comparing (38) and (47), if the following condition is satisfied, the estimation errors of the continuous-time states (\mathbf{e}_{cp}) completely match those of the discrete-time states (\mathbf{e}_{dp}) at every sampling period:

$$e^{(\mathbf{A}_{cp} - \mathbf{K}_{cp} \mathbf{C}_{cp})T} = \mathbf{A}_{dp} - \mathbf{K}\mathbf{C}. \quad (49)$$

From (32), the necessary and sufficient condition for the existence of \mathbf{K} in (49) is given by

$$\text{rank } \mathbf{C} = \text{rank} \begin{bmatrix} \mathbf{C} \\ \mathbf{A}_{dp} - e^{(\mathbf{A}_{cp} - \mathbf{K}_{cp} \mathbf{C}_{cp})T} \end{bmatrix}. \quad (50)$$

Concerning the matrix \mathbf{C} , the next theorem is proved in [14], if ρ_q is the generalized observability controllability index. In the case of the observability index, it is also proved in [17].

Theorem 2: Let $(\mathbf{A}_{cp}, \mathbf{C}_{cp})$ be an observable pair. If the output multiplicities satisfy $M_q \geq \rho_q$ for ($q = 1, 2, \dots, p_p$), for almost all ν_{qk} ($k = 1, \dots, M_q$) and almost all T , the matrix \mathbf{C} has full column rank, i.e.,

$$\text{rank } \mathbf{C} = n_p. \quad (51)$$

Because of this theorem, the column rank \mathbf{C} in (50) is full, thus, (50) is satisfied. As a result, the existence of \mathbf{K} in (49) is assured. Solving (49), the redesigned parameters are given by

$$\mathbf{K} = (\mathbf{A}_{dp} - e^{(\mathbf{A}_{cp} - \mathbf{K}_{cp} \mathbf{C}_{cp})T}) \mathbf{C}^{-1}. \quad (52)$$

In this section, only the redesign method for a full-order observer was presented. However, a minimum-order observer can also be redesigned the same way as above [14].

V. ILLUSTRATIVE EXAMPLE

A. Digital Redesign of a Position Control System for a DC Servo Motor with the Disturbance Observer

In this section, simulation and experimental results for a position control system for a dc servo motor with the disturbance observer are presented, as shown in Fig. 8.

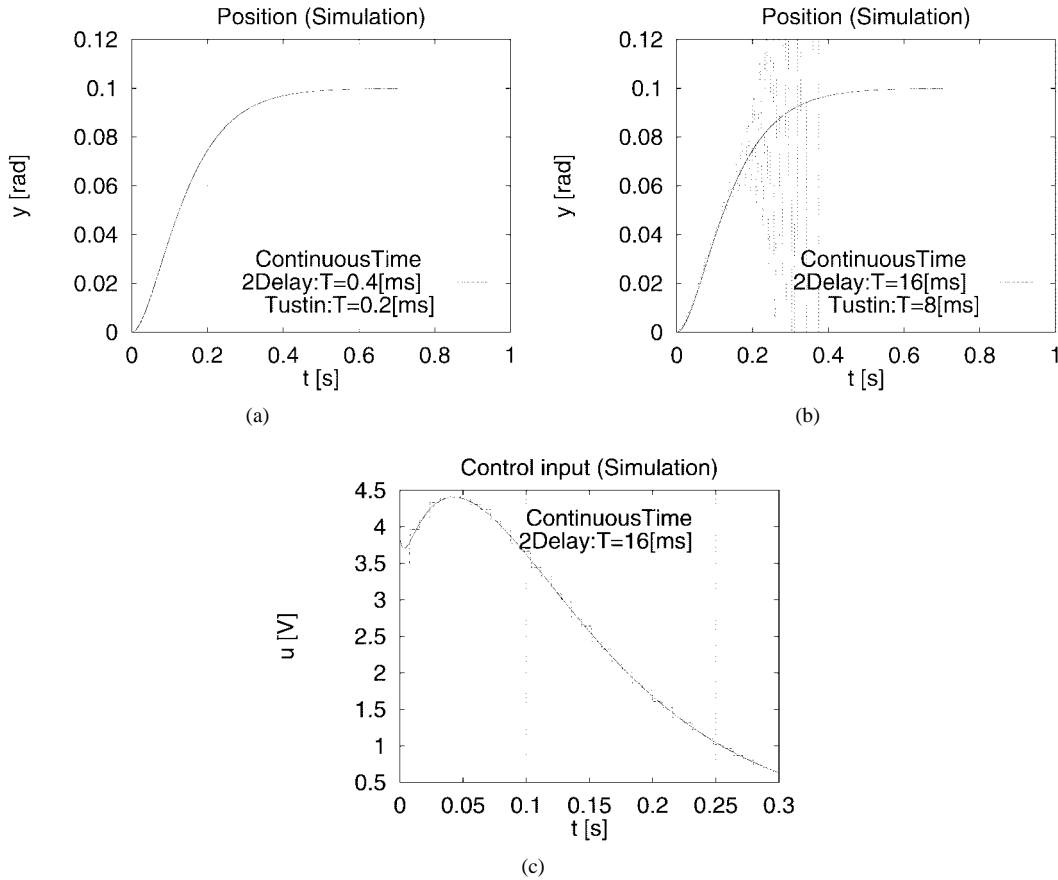


Fig. 9. Simulation results. (a) Position responses ($T_u = 0.2$ [ms]). (b) Position responses ($T_u = 8$ [ms]). (c) Control input responses ($T_u = 8$ [ms]).

In the experiments, a digital signal processor (DSP) (NEC: μ PD77230, 32-b floating point) is used, and the 1/100 gear ratio dc servo motor is driven by a 10-kHz switching frequency MOSFET chopper. The pulse counter generates 1000 pulses/rev on the motor shaft (it becomes 100 000 pulses/rev on the geared shaft), and the speed is detected through a 12-b A/D converter by a tachometer [18].

In the design of the continuous-time controller, assuming that the disturbance torque d is a step-function type, the plant is represented by

$$\begin{aligned} \dot{\mathbf{x}}_{cp}(t) &= \mathbf{A}_{cp}\mathbf{x}_{cp}(t) + \mathbf{b}_{cp}u_{cp}(t) \\ \mathbf{A}_{cp} &= \begin{bmatrix} 0 & 1 & 0 \\ 0 & -\frac{B}{J} & -\frac{1}{J} \\ 0 & 0 & 0 \end{bmatrix} \\ \mathbf{b}_{cp} &= \begin{bmatrix} 0 \\ \frac{K}{J} \\ 0 \end{bmatrix} \\ \mathbf{x}_{cp} &= \begin{bmatrix} \theta \\ \omega \\ d \end{bmatrix} \end{aligned} \quad (53)$$

where θ is angular position, ω is angular velocity, and u_{cp} is the dc voltage of the motor terminal. Assuming the command

input r to be a step-function type, the continuous-time control law is given by

$$\begin{aligned} u_{cp} &= K_p(r - \theta) - K_d\omega + \frac{1}{K_n}\hat{d} \\ &= \mathbf{f}_{cp}\hat{\mathbf{x}}_{cp} + g_{cp}r \end{aligned} \quad (54)$$

where $\mathbf{f}_{cp} = [-K_p, -K_d, (1/K_n)]$, $g_{cp} = K_p$, $\hat{\mathbf{x}}_{cp} = [\theta, \omega, \hat{d}]^T$. As shown in Fig. 8, the continuous-time disturbance observer is also given by

$$\begin{aligned} \dot{\hat{v}}_c(t) &= \hat{A}_c\hat{v}_c(t) + \hat{B}_c\omega(t) + \hat{J}_c u_{cp}(t) \\ \hat{d}(t) &= \hat{v}_c(t) + l_c\omega(t) \end{aligned} \quad (55)$$

where $\hat{A}_c = -\omega_c$, $\hat{B}_c = -B_n\omega_c + J_n\omega_c^2$, $\hat{J}_c = K_n\omega_c$, and ω_c is the cutoff frequency of the low-pass filter.

The nominal values of the plant constants are $J_n = 0.0730$ [kg·m²], $B_n = 3.26$ [kg·m²/s], $K_n = 0.388$ [N·m/V]. Also, the continuous-time control parameters are $K_p = 8.91$, $K_d = -4.99$, $\omega_c = 300$ [rad/s].

First, the control law (54) is redesigned. Although $(\mathbf{A}_{cp}, \mathbf{B}_{cp})$ in (53) is not controllable, the conditions in (33) are satisfied for input multiplicity $N_1 = 2$. From (35), the redesigned control laws for $T = 0.4$ [ms], $\mu_{11} = 0.5$ are obtained, as follows:

$$\begin{bmatrix} \mathbf{u}_{11}[i] \\ \mathbf{u}_{12}[i] \end{bmatrix} = \begin{bmatrix} -33.4 & 3.38 & 2.57 \\ -33.6 & 3.34 & 2.57 \end{bmatrix} \begin{bmatrix} \theta[i] \\ \omega[i] \\ \hat{d}[i] \end{bmatrix} + \begin{bmatrix} 33.4 \\ 33.6 \end{bmatrix} \mathbf{r}[i].$$

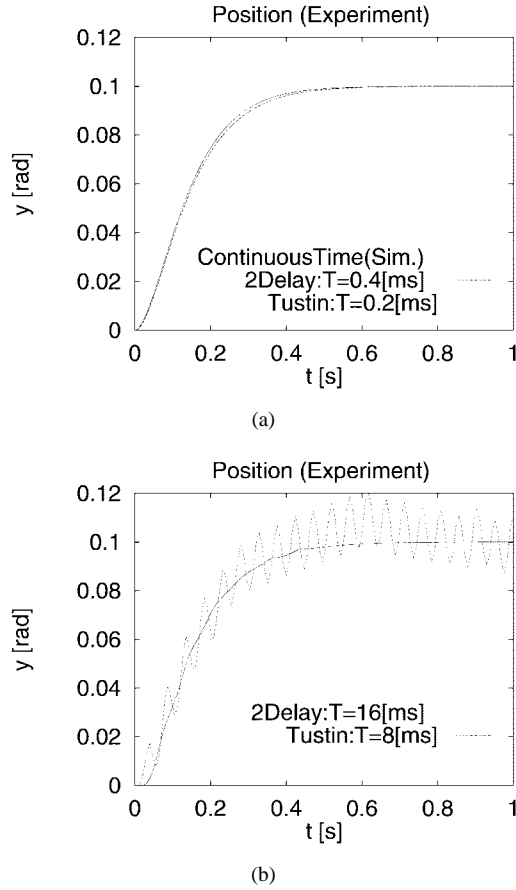


Fig. 10. Experiment results. (a) Position responses ($T_u = 0.2$ [ms]). (b) Position responses ($T_u = 8$ [ms]).

Second, the disturbance observer in (55) is redesigned the same way as in Section IV. For $T = 0.4$ [ms], $\mu_{11} = 0.5$, the discrete-time observer is obtained [14] by

$$\begin{aligned}\hat{v}_d[i+1] &= \hat{A}_d \hat{v}_d[i] + \hat{B}_d \omega[i] + \hat{J}_{11} u_{11}[i] + \hat{J}_{12} u_{12}[i] \\ \hat{d}[i] &= \hat{v}_d[i] + l_d \omega[i]\end{aligned}\quad (56)$$

where $l_d = -20.8$, $\hat{A}_d = 0.887$, $\hat{B}_d = 1.99$, $\hat{J}_{11} = 2.12 \times 10^{-2}$, and $\hat{J}_{12} = 2.21 \times 10^{-2}$.

B. Simulations and Experiments

Because the redesign system uses the two-delay input control, the output sampling period T is twice as long as the input sampling period T_u . In the following simulations and experiments, the proposed method (two-delay) is compared with the Tustin (bilinear) transformation at the same input sampling period so that the calculation costs of the two systems may be equal and these comparisons may be fair. Therefore, the output sampling period of the proposed method is twice as long as that of the Tustin transformation.

Simulated and experimental results are shown in Figs. 9 and 10. In the very short sampling period (0.2 ms), we find the Tustin transformation and the proposed transformation have almost the same time responses. However, in the long sampling period (8 ms), as shown in Figs. 9 and 10(b), the

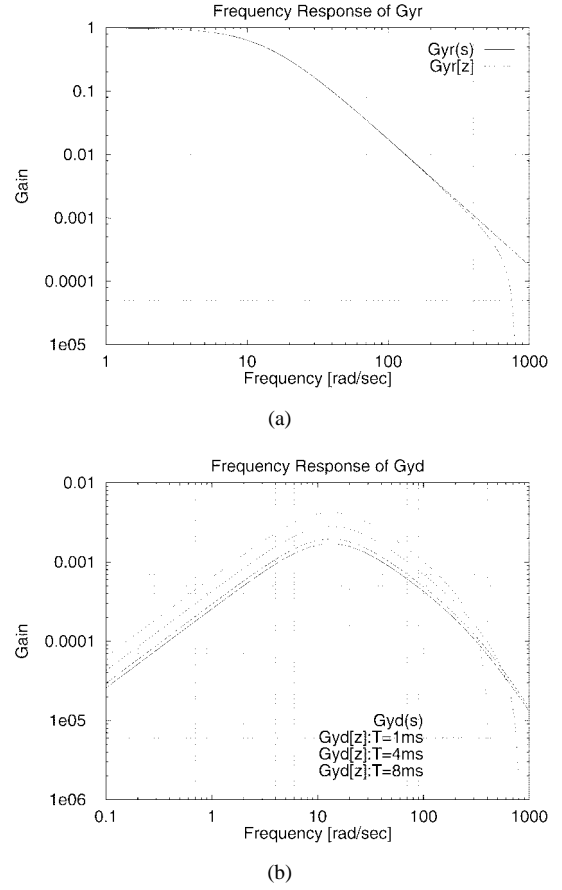


Fig. 11. Frequency responses. (a) Command responses (G_{yr}). (b) Disturbance responses (G_{yd}).

proposed method gives better performance than the Tustin transformation. While the responses of the Tustin transformation are unstable, those of the proposed method are stable, and exactly match the continuous-time responses.

The simulated time responses of the control input are shown in Fig. 9(c), which indicates that the control input of the proposed method is smooth in spite of using two-delay control.

C. Frequency Responses of the Closed-Loop Systems

The frequency responses from the command input (r) to the plant state (θ) are shown in Fig. 11(a). In the wide frequency band lower than the Nyquist frequency, the discrete-time response redesigned by the proposed method ($G_{yr}[z]$) matches the continuous-time response ($G_{yr}(s)$). That is guaranteed by (17), (28), (30), and (31).

The frequency responses from the disturbance torque (d) to the plant state (θ) are also shown in Fig. 11(b), which indicates that the longer the sampling period is, the poorer the disturbance rejection performance is. Therefore, Fig. 11(b) tells us the practical limitations of the sampling period for the proposed method.

D. Disturbance Responses

The simulation results of the time responses under the step function type disturbances (5 N·m, $t > 1$ s) are shown in Fig. 12. In the short sampling period (0.4 ms), the redesigned

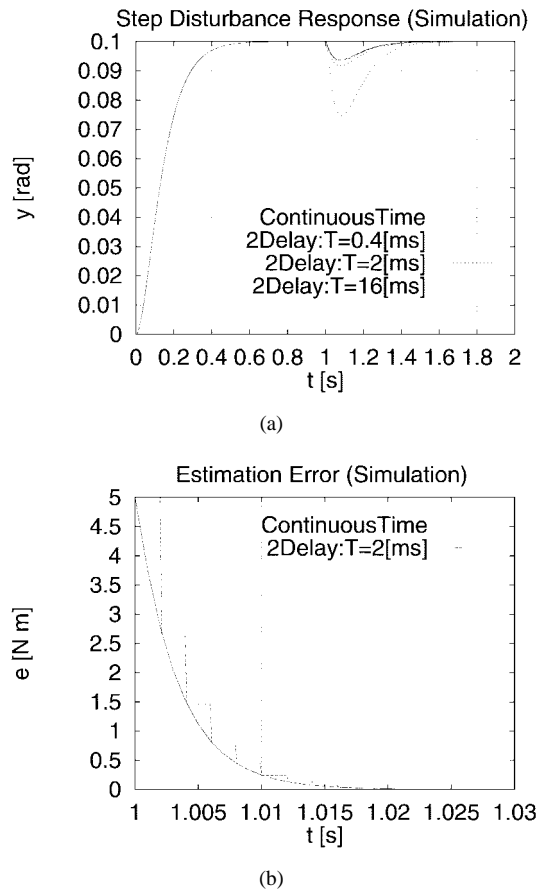


Fig. 12. Responses under step-type disturbance (simulation). (a) Position responses. (b) Estimation errors of disturbance.

system has almost as same performance as the continuous-time system. However, as mentioned above [Fig. 11(b)], the disturbance rejection performance becomes poor in the large sampling period (16 ms).

The time responses of the disturbance estimation errors are shown in Fig. 12(b). The estimation error of the discrete-time observer which is obtained by the proposed method completely matches that of the continuous-time observer at every sampling period. Fig. 12(b) also assures that there is no offset between the command input (r) and the plant state (θ).

VI. CONCLUSION

New digital redesign methods, both for a controller and an observer using N-delay control, were proposed. A position control using a dc servo motor was selected for an example, and simulations and experiments were performed, which indicated that the proposed method had better performance than that of the Tustin transformed digital controller. Moreover, the disturbance rejection performance is considered both in the time and frequency domains.

One of the remarkable advantages of the proposed method is that the states of the sampled-data system completely become equal to those of the continuous-time system, independent of sampling period and, as a result, the stability of the redesigned system is assured. The choice of μ_j (a parameter of the input sampling period) should be further investigated for the

optimal intersample time responses and the smoothest control inputs.

The proposed method can be extended to be applicable to the output-feedback-type problem, which will be presented at the next opportunity.

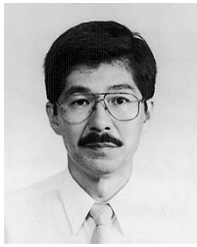
REFERENCES

- [1] B. C. Kuo, *Digital Control Systems*. New York: Holt, Rinehart and Winston, 1980.
- [2] B. C. Kuo and D. W. Peterson, "Optimal discretization of continuous-data control system," *Automatica*, vol. 9, no. 1, pp. 125–129, 1973.
- [3] L. S. Shieh, J. L. Zhang, and S. Ganesan, "Pseudo-continuous-time quadratic regulators with pole in a specific region," *Proc. Inst. Elect. Eng.*, vol. 137, pt. D, no. 5, pp. 297–301, 1990.
- [4] R. A. Yackel, B. C. Kuo, and G. Singh, "Digital redesign of continuous systems by matching of states at multiple sampling periods," *Automatica*, vol. 10, no. 1, pp. 105–111, 1974.
- [5] L. S. Shieh, X. M. Zhao, and J. L. Zhang, "Locally optimal-digital redesign of continuous-time systems," *IEEE Trans. Ind. Electron.*, vol. 36, pp. 511–515, Aug. 1989.
- [6] K. S. Rattan, "Digitalization of continuous control systems," *IEEE Trans. Automat. Contr.*, vol. 29, pp. 282–285, Mar. 1984.
- [7] ———, "Compensating for computational delay in digital equivalent of continuous control systems," *IEEE Trans. Automat. Contr.*, vol. 34, pp. 895–899, Aug. 1989.
- [8] J. P. Keller and B. D. O. Anderson, "A new approach to the discretization of continuous-time controllers," *IEEE Trans. Automat. Contr.*, vol. 37, pp. 214–223, Feb. 1992.
- [9] M. Araki, "Recent developments in digital control theory," in *Proc. IFAC World Congr.*, July 1993, vol. 9, pp. 251–260.
- [10] M. Araki and T. Hagiwara, "Pole assignment by multirate-data output feedback," *Int. J. Control*, vol. 44, no. 6, pp. 1661–1673, 1986.
- [11] K. L. Moore, S. P. Bhattacharyya, and M. Dahleh, "Capabilities and limitations of multirate control schemes," *Automatica*, vol. 29, no. 4, pp. 941–951, 1993.
- [12] H. Fujimoto, A. Kawamura, and M. Tomizuka, "Proposal of generalized digital redesign method in use of N-delay control," in *Proc. Amer. Control Conf.*, June 1997, pp. 3290–3294.
- [13] T. Mita, Y. Chida, Y. Kazu, and H. Numasato, "Two-delay robust digital control and its applications—Avoiding the problem on unstable limiting zeros," *IEEE Trans. Automat. Contr.*, vol. 35, pp. 962–970, Aug. 1990.
- [14] H. Fujimoto and A. Kawamura, "New digital redesign method in use of N-Delay control (in Japanese)," *Trans. IEEJ*, vol. 117-D, no. 5, pp. 645–654, 1997.
- [15] P. Lancaster and M. Tismenetsky, *The Theory of Matrices*. New York: Academic, 1985.
- [16] T. Chen and B. Francis, *Optimal Sampled-Data Control Systems*. Berlin, Germany: Springer-Verlag, 1995.
- [17] T. Hagiwara and M. Araki, "Design of a state state feedback controller based on the multirate sampling of plant output," *IEEE Trans. Automat. Contr.*, vol. 33, pp. 812–819, Sept. 1988.
- [18] A. Kawamura, K. Miura, and T. Ishizawa, "Observer based sliding mode CP control of two axis scara robot," in *Proc. IEEE Int. Workshop Advanced Motion Control*, Mar. 1990, pp. 197–202.



Hiroshi Fujimoto (S'99) was born in Tokyo, Japan, in 1974. He received the B.S. and M.S. degrees in electrical and computer engineering from Yokohama National University, Yokohama, Japan, in 1996 and 1998, respectively. He is currently working towards the Ph.D. degree in the Department of Electrical Engineering, University of Tokyo, Tokyo, Japan.

He has also been a Research Fellow of the Japan Society for the Promotion of Science since 1998. His interests include control engineering, motion control, and digital control. His current work concerns a multirate sampled-data system for a new digital motion control method. Mr. Fujimoto is a student member of the Institute of Electrical Engineers of Japan.



Atsuo Kawamura (S'77–M'78–SM'96) was born in Yamaguchi Prefecture, Japan, in 1953. He received the B.S.E.E., M.S.E.E., and Ph.D. degrees in electrical engineering from the University of Tokyo, Tokyo, Japan, in 1976, 1978, and 1981, respectively.

In 1981, he joined the Department of Electrical and Computer Engineering, University of Missouri, Columbia, as a Postdoctoral Fellow, and was an Assistant Professor there from 1983 to 1986. He joined the Department of Electrical and Computer Engineering, Yokohama National University, Yokohama, Japan, in 1986 as an Associate Professor and, in 1996, he became a Professor. His interests include power electronics, digital control, electric vehicles, robotics, and ultrasonic actuators. He will serve as the Technical Program Chairman of the Workshop on Advanced Motion Control in 2000 (AMC2000).

Prof. Kawamura received the IEEE TRANSACTIONS ON INDUSTRY APPLICATIONS Prize Paper Award in 1988 and the Prize Paper Award from the Institute of Electrical Engineers of Japan (IEEJ) in 1996. He was the Conference Chairperson of the IEEE Industry Applications Society and IEEJ Industry Applications Society Joint Power Conversion Conference (PCC-Yokohama) in 1993. He served as the Technical Program Chairman of the IEEE Power Electronics Specialist Conference (PESC'98) in 1998. He has been an Associate Editor of the IEEE TRANSACTIONS ON POWER ELECTRONICS since 1995. He is a member of the IEEJ, Robotics Society of Japan, Institute of Electronics, Information and Communication Engineers, and Society of Instrument and Control Engineering.

Masayoshi Tomizuka (M'86–SM'95–F'97), for a photograph and biography, see p. 24 of the March 1999 issue of this TRANSACTIONS.